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April 5, 2002

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner of Patents, Washington, D.C. 20231 on 4/5/02

*Shaylor E. Dunn*  
Shaylor E. Dunn  
Date of Signature 4/5/02

Commissioner for Patents  
Washington, D.C. 20231

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APR 11 2002

Re: U.S. Patent Application Serial No. 09/981,858 for **Technology Center 2600**  
**METHODS AND SYSTEMS FOR FAST PACKET FORWARDING**  
Our File No. 297/144/2

Sir:

Please find enclosed the following:

1. A Supplemental Information Disclosure Statement (3 pages);
2. Form PTO/SB/08B (2 pages) in duplicate;
3. Thirteen (13) cited references; and
4. A return-receipt postcard to be returned to our offices with the U.S. Patent and Trademark filing stamp thereon.

Please contact our offices if there are any questions.

Respectfully submitted,

JENKINS & WILSON, P.A.

*Gregory A. Hunt*

Gregory A. Hunt  
Registration No. 41,085  
Customer No. Bar Code Label:

GAH/sed

Enclosures



25297

PATENT TRADEMARK OFFICE



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Shayor E. Dunn  
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Technology Center 2600

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Mehrotra et al.

Group Art Unit: 2661

**Serial No.: 09/981,858**

Examiner: Not Assigned

Filed: October 18, 2001

Docket No.: 297/144/2

Confirmation No.: 8294

For: METHODS AND SYSTEMS FOR FAST PACKET FORWARDING

\*\*\*\*\*

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.56, 1.97, and 1.98, applicants' undersigned attorney brings to the attention of the Patent and Trademark Office the following references. Copies of the references as well as Forms PTO/SB/08A and PTO/SB/08B are attached hereto. This is not to be construed as a representation that a search has been made or that a reference is relevant merely because cited.

Publication by Michigan University and Merit Network entitled "Internet Performance Measurement and Analysis Project," <http://nic.merit.edu/ipma>, pp. 1-17, (2002).

Publication by Nick McKeown entitled "How Scalable is the Capacity of (Electronic) IP Routers," Optical Fiber Communication Conference, pp. 1-36, (March 2001).

Publication by Chiueh et al. entitled "Cache Memory Design for Network Processors," Proceedings of Sixth International Symposium on High-Performance Computer Architecture, pp. 1-10, (2000).

Publication by Yazdani et al. entitled "Fast Scalable Schemes for the IP Address Lookup Problem," Proc. IEEE Conference on High Performance Switching and Routing, pp. 83-92, (2000).

Although it is believed that no fee is due, the Commissioner is hereby authorized to charge any deficiencies of payment associated with the filing of this Supplemental Information Disclosure Statement to Deposit Account No. 50-0426.

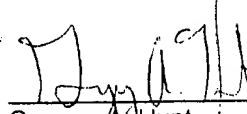
Respectfully submitted,

JENKINS & WILSON, P.A.

Date:

4/5/02

By:



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Enclosures

Publication by Wei et al. entitled "Just-In-Time Signaling for WDM Optical Burst Switching Networks," Journal of Lightwave Technology, Vol. 18, No. 12, pp. 2019-2037, (December 2000).

Publication by Listanti et al. entitled "Architectural and Technological Issues for Future Optical Internet Networks," IEEE Communications Magazine, pp. 82-92, (September 2000).

Publication by Chiueh et al. entitled "High-Performance IP Routing Table Lookup Using CPU Caching," Proc. IEEE INFO-COM '99, pp. 1421-1428, (1999).

Publication by McKeown et al. entitled "High Performance Switches and Routers: Theory and Practice," Hot Interconnects Tutorial Slides, <http://tiny-tera.stanford.edu/nickm/talks/index.html>, pp. 1-126, (August 1999).

Publication by Nilsson et al. entitled "IP-Address Lookup Using LC-Tries," IEEE Journal on Selected Areas in Communications, Vol. 17, No. 6, pp. 1083-1092, (June 1999).

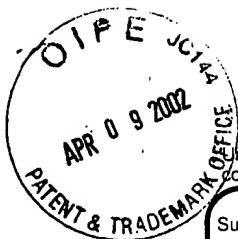
Publication by Huang et al. entitled "A Novel IP-Routing Lookup Scheme and Hardware Architecture for Multigigabit Switching Routers," IEEE Journal on Selected Areas in Communications, Vol. 17, No. 6, pp. 1093-1104, (June 1999).

Publication by Gupta et al. entitled "Routing Lookups in Hardware at Memory Access Speeds," Proc. IEEE INFOCOM'98, pp. 1240-1247, (1998).

Publication by J.M. Rabney entitled "Digital Integrated Circuits, A Design Perspective," Prentice Hall, Ch. 8, pp. 446-454, (1996).

Publication by Keith Sklower entitled "A Tree-Based Packet Routing Table for Berkeley Unix," Technical Report, pp. 1-14.

Early passage of the subject application to issue is earnestly solicited.



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PTO/SB/08B (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449B/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

### Complete if Known

Application Number 09/981,858  
Filing Date 10/18/2001  
First Named Inventor Pronita Mehrotra  
Group Art Unit 2661  
Examiner Name Not Assigned  
Attorney Docket Number 297/144/2

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### OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Technology Center 2600

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	B1	MICHIGAN UNIVERSITY AND MERIT NETWORK, "Internet Performance Measurement and Analysis Project," <a href="http://nic.merit.edu/ipma">http://nic.merit.edu/ipma</a> , p. 1-17, (2002).	X
	B2	NICK MCKEOWN, "How Scalable is the Capacity of (Electronic) IP Routers," Optical Fiber Communication Conference, p. 1-36, (March 2001).	X
	B3	CHIUEH ET AL., "Cache Memory Design for Network Processors," Proceedings of Sixth International Symposium on High-Performance Computer Architecture, p. 1-10, (2000).	X
	B4	YAZDANI ET AL., "Fast Scalable Schemes for the IP Address Lookup Problem," Proc. IEEE Conference on High Performance Switching and Routing, p. 83-92, (2000).	X
	B5	WEI ET AL., "Just-In-Time Signaling for WDM Optical Burst Switching Networks," Journal of Lightwave Technology, Vol. 18 (No. 12), p. 2019-2037, (December 2000).	X
	B6	LISTANTI ET AL., "Architectural and Technological Issues for Future Optical Internet Networks," IEEE Communications Magazine, p. 82-92, (September 2000).	X
	B7	CHIUEH ET AL., "High-Performance IP Routing Table Lookup Using CPU Caching," Proc. IEEE INFO-COM '99, p. 1421-1428, (1999).	X
	B8	MCKEOWN ET AL., "High Performance Switches and Routers: Theory and Practice," Hot Interconnects Tutorial Slides, <a href="http://tiny-tera.stanford.edu/nickm/talks/index.html">http://tiny-tera.stanford.edu/nickm/talks/index.html</a> , p. 1-126, (1999).	X
	B9	NILSSON ET AL., "IP-Address Lookup Using LC-Tries," IEEE Journal on Selected Areas in Communications, Vol. 17 (No. 6), p. 1083-1092, (June 1999).	X
	B10	HUANG ET AL., "A Novel IP-Routing Lookup Scheme and Hardware Architecture for Multigigabit Switching Routers," IEEE Journal on Selected Areas in Communications, Vol. 17 (No. 6), p. 1093-1104, (June 1999).	X
	B11	GUPTA ET AL., "Routing Lookups in Hardware at Memory Access Speeds," Proc. IEEE INFOCOM'98, p. 1240-1247, (1998).	X

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	09/981.858
		Filing Date	10/18/2001
		First Named Inventor	Pronita Mehrotra
		Group Art Unit	2661
		Examiner Name	Not Assigned
		Attorney Docket Number	297/144/2
Sheet	2	of	2

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**Technology Center 2600**

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	B12	J.M. RABNEY, "Digital Integrated Circuits, A Design Perspective," Prentice Hall, Ch. 8, p. 446-454, (1996).	X
	B13	KEITH SKLOWER, "A Tree-Based Packet Routing Table for Berkeley Unix," Technical Report, p. 1-14.	X

Examiner Signature		Date Considered	
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